BIRLA INSTITUTE OF TECHNOLOGY, MESRA, RANCHI

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Advertisement for Post of Junior Research Fellow/Project Associate I

Applications are invited for the post of Junior Research Fellow (JRF)/ Project Associate I in the Department of Electronics and Communication Engineering, Birla Institute of Technology Mesra for SERB POWER funded project titled "Scalable Assembly of Painless Hollow Microneedle Array".

Principal Investigator : Dr.Richa Mishra, Assistant Professor, ECE Dept., BIT Mesra
Mobile - 9002248104, email - richa@bitmesra.ac.in

Job duration: 1 year extendable up to 3 years based on annual progress

Number of posts: 1 (JRF or Project Associate I)

Eligibility:

ME/M.Tech/BE/B.Tech with GATE qualification in Electronics and Communication (**For JRF**).
ME/M.Tech in Electronics and Communication. Qualifying degree with at least 75 % marks. The candidate should have a strong inclination towards automation and product development based research with knowledge of Comsol Multiphysics simulation, CAD design, 3D printing and material characterization techniques. Enrolment in Ph.D. program would be desired.

Please Note: Preference will be given for JRF position and on unavailability of JRF, Project Associate I will be considered.

Fellowship: (a) For Gate Qualified - Rs. 31000/- (+16% HRA) per month for first two years, Rs. 35000/-(+16% HRA) pm for 3rd year (For JRF).

(b) For Non Gate Qualified - Rs. 25000/-+ 16 % HRA) per month for first two years & 28,000/-+ 16 % HRA pm for 3rd year (For Project Associate I)

How to apply: Send your application with an updated CV through e-mail to richa@bitmesra.ac.in with copy to ar.src@bitmesra.ac.in by **11th January 2023**. The subject of the email should be: Application for the post of JRF or Project Associate I. Candidates are advised to also send the scanned copy of certificates; grade cards/mark sheets, publications, photo-identity proof etc. as a **single pdf file** with the application. The selected candidates shall be intimated by email on 12th January 2023.

Venue, date & time of interview: The **on-line/offline interview will be held on 17th January 2023**, in the Department of Electronics and Communication Engineering at 10:00 AM. No TA/DA will be provided for attending the interview.

Registrar