

**Student
TIME TABLE**

**BIRLA INSTITUTE OF TECHNOLOGY
MESRA, RANCHI
TIME TABLE
MO-2022**



w.e.f.:08.08.2022

Department: ECE		Programm: M.Tech.				Branch/Section: VLSI		Semester: II		
Days	Room No.: 254, ESL									
	I 08:00-08:50	II 09:00-09.50	III 10:00-10.50	IV 11:00-11.50	V 12:00-12.50	VI 13:30-14.20	VII 14:30-15.20	VIII 15:30-16.20	IX 16:30-17.20	
Monday	DVLSI/AI (ESL)	AVLSI/KK (ESL)	ES/VKSY/254	BSP/ PPD	PC/SST	Lunch Break				
Tuesday	DVLSI/AI (ESL)	AVLSI/KK (ESL)	ES/VKSY/254	BSP/ PPD	PC/SST				ICT/RM	NEDM/SAP
Wednesday	DVLSI/AI (ESL)	AVLSI/KK (ESL)	ES/VKSY/254	ADSP/SSP	PC/SST			ESL/VKSY		
Thursday	ICT/RM	NEDM/SAP						Communication Skills-I		
Friday			VLSID LAB/AI					ICT/RM	NEDM/SAP	

Course Code	Course Title	Credit	Teacher
EC581	Analog VLSI Dsign (AVLSI)	3	Dr. A.Islam (AI)
EC583	Digital VLSI Dsign (DVLSI)	3	Dr. Kalyan Koley (KK)
EC585	Embedded Systems (ES)	3	Dr. Vimal K.S. Yadav (VKSY)
EC587	IC Technology (ICT)	3	Dr. Richa Mishra (RM)
EC592R1	Nanoelectronics Devices and Materials (NEDM)	3	Dr. S.Prasad (SAP)
EC582	VLSI Design Lab (VLSID LAB)	2	Dr. A.Islam (AI)
EC586	Embedded Systems Lab (ESL)	2	Dr. Vimal K.S. Yadav (VKSY)
MT133	Communication Skills-I	1.5	Dr. Rohit Pandey(RP)

(Prem Prakash)
Co-ordinator
Central Time Table, BIT Mesra

Department Time Table
Co-ordinator

HOD

(Dr. Nisha Gupta)
Dean (PG Studies)